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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Complete if Known Substitute for form 1449/PTO **Application Number** Filing Date INFORMATION DISCLOSURE First Named Inventor DAVID H. SHEN STATEMENT BY APPLICANT Art Unit (Use as many sheets as necessary) **Examiner Name**

Attorney Docket Number

Sheet 1 U. S. PATENT DOCUMENTS Name of Patentee or Pages, Columns, Lines, Where Examiner **Document Number** Publication Date Relevant Passages or Relevant Figures Appear MM-DD-YYYY Applicant of Cited Document Initials* No. Number-Kind Code^{2 (F known)} ^{US-} 6,268,815 **GUSTAVSSON** /TM/ 1 07-31-2001 whole patent ^{US-} 5,225,798 HUNSINGER 2 07-06-1993 /TM/ whole patent US- 5,212,659 05-18-1993 SCOTT /TM/ 3 whole patent USus-US-TIS. US-IIS. US-US-USusus-US-ับร-ÜS-ÜS-US-

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ Number ⁴ "Kind Code ⁶ (# known)	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/TM/	1	OPPENHEIM et. al., "Discrete-Time Signal Processing," Prentice Hall, USA,1989, pp. 300-328.	
/TM/	2	GREGORIAN et. al., "Switched-Capacitor Circuit Design," Proceedings of IEEE, vol. 71, no. 8, USA, pp. 941-966, Aug. 1983.	
/TM/	3	CASTELLO et. al., "Performance Limitations in Switched-Capacitor Filters, "IEEE Transactions on Circuits and Systems, vol. CAS-32,no. 9, USA,	
		pp. 865-876, Sept. 1985.	
/TM/	4	MARTIN et. al., "Effects of the Op Amp Finite Gain and Bandwidth on the Performance of Switched-Capacitor Filters," IEEE Transactions on Circuits	
		and Systems, vol. CAS-28, no. 8, USA, pp. 822-829, Aug. 1981.	
/TM/	5	KOLI et. al., "A fully differential class-AB switched-current integrator for signal processing," IEEE Journal of Solid-State Circuits, vol. 32, no. 2, USA,	
·		Feb. 1997, pp. 238 - 244.	
/TM/	6	MUHAMMAD et. al., "A Discrete-Time Bluetooth Receiver in a 0.13um Digital CMOS Process," Proceedings of the 2004 IEEE International Solid-State	
		Circuits Conference, USA, pp. 268-269.	

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